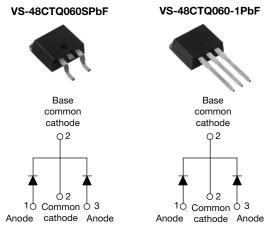


Vishay High Power Products

Schottky Rectifier, 2 x 20 A



D²PAK

TO-262

PRODUCT SUMMARY			
I _{F(AV)}	2 x 20 A		
V _R	60 V		

FEATURES

- 150 °C T_J operation
- Center tap configuration
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Halogen-free according to IEC 61249-2-21 definition
- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	40	А			
V _{RRM}		60	V			
I _{FSM}	t _p = 5 μs sine	1000	А			
V _F	20 Apk, $T_J = 125 \text{ °C}$ (per leg)	0.58	V			
TJ	Range	- 55 to 150	°C			

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-48CTQ060SPbF VS-48CTQ060-1PbF	UNITS	
Maximum DC reverse voltage	V _R	60	M	
Maximum working peak reverse voltage	V _{RWM}	00	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per leg		50 % duty cycle at T_{C} = 111 °C, rectangular waveform		20	
See fig. 5 per device			rectangular wavelonn	40	А
Maximum peak one cycle non-repetitive surge current per leg	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	1000	
See fig. 7		10 ms sine or 6 ms rect. pulse		260	
Non-repetitive avalanche energy per leg E_{AS} $T_J = 25 \text{ °C}, I_{AS}$		T _J = 25 °C, I _{AS} = 1.50 A, L = 11.8	°C, I _{AS} = 1.50 A, L = 11.5 mH		mJ
Repetitive avalanche current per leg		Current decaying linearly to zero in 1 μs Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.50	А

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	20 A	T _J = 25 °C	0.61	V
		40 A		0.83	
		20 A	T _J = 125 °C	0.58	
		40 A		0.75	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	2	mA
See fig. 2		T _J = 125 °C		89	
Threshold Voltage	V _{F(TO)}	T _J =T _J maximum		0.37	V
Forward slope resistance	r _t			8.26	mΩ
Maximum junction capacitance per leg	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 °C		1220	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

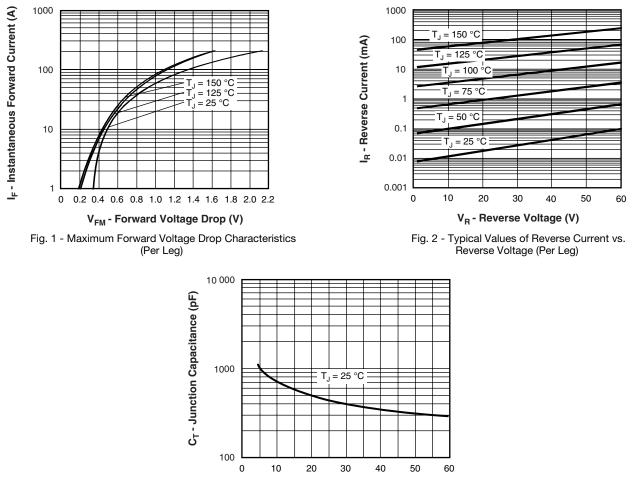
Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	9	T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to case per leg		P		2.0	
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	1.0	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	S Mounting surface, smooth and greased		
A				2	g
Approximate weight			0.07	oz.	
Mounting torque -	minimum			6 (5)	kgf · cm
	maximum			12 (10)	(lbf · in)
Marking device			Case style D ² PAK	48CT0	2060S
			Case style TO-262	48CTC	060-1



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V_R - Reverse Voltage (V)

Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

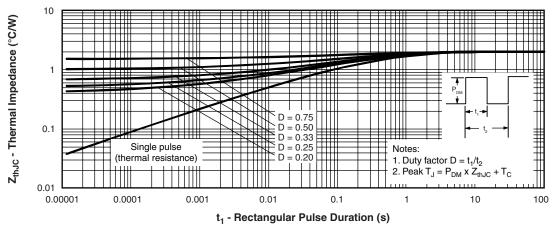
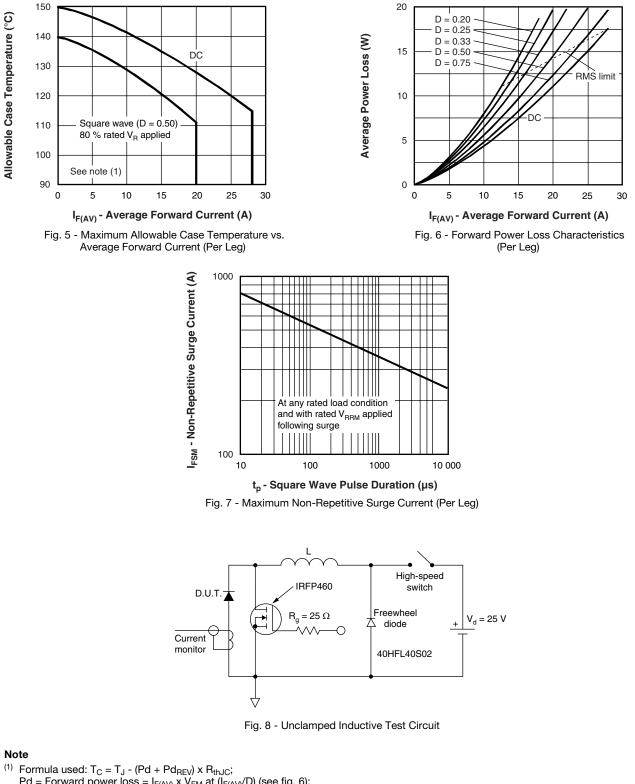


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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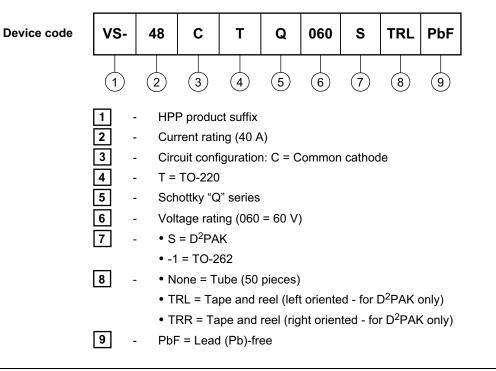
 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{10} \ \mathsf{V} \end{array}$





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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95014</u>					
Part marking information www.vishay.com/doc?95008					
Packaging information	www.vishay.com/doc?95032				



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